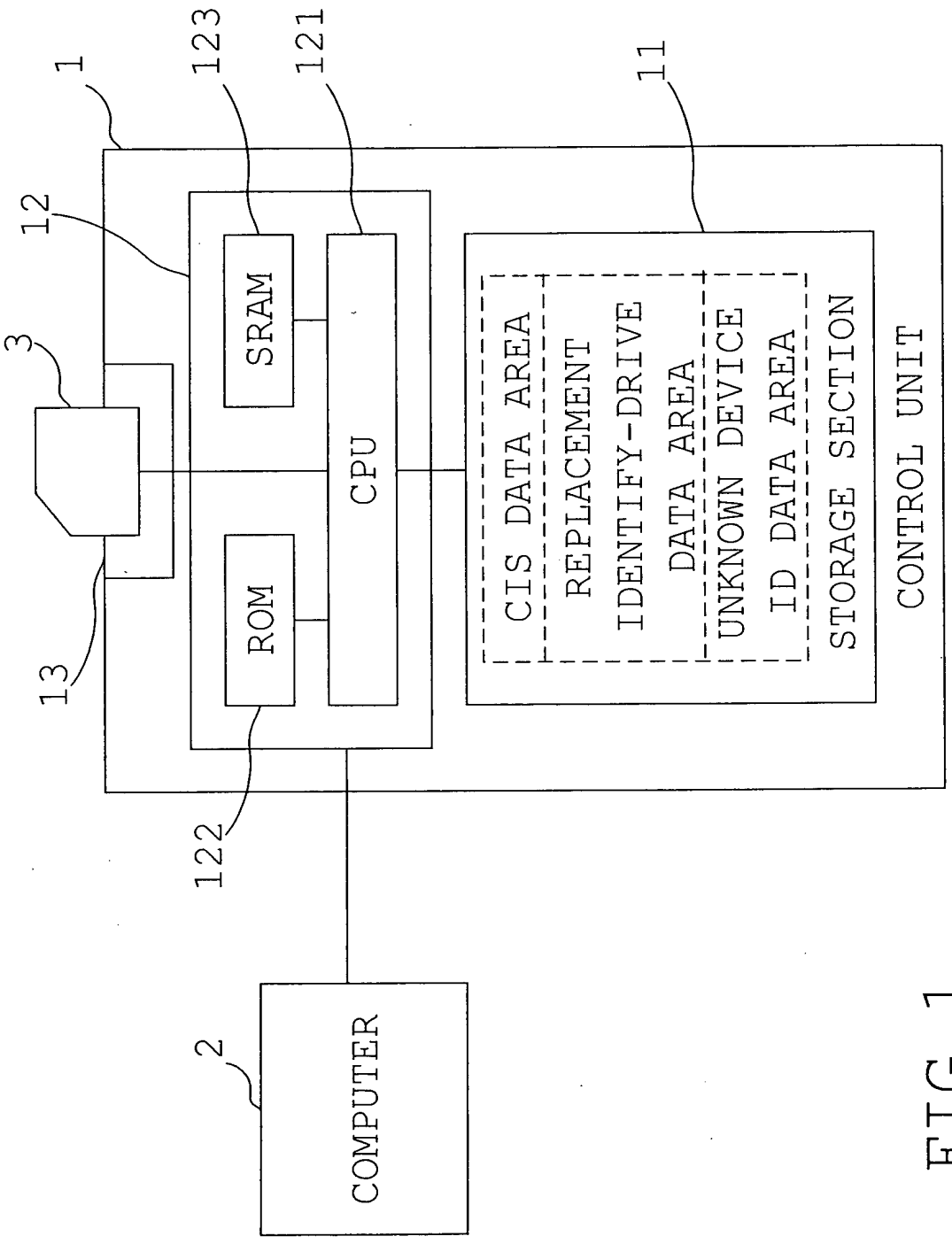

1
123
121
11
12
3
SRAM
CPU
CIS DATA AREA
REPLACEMENT
IDENTIFY-DRIVE
DATA AREA
UNKNOWN DEVICE
ID DATA AREA
STORAGE SECTION
CONTROL UNIT
13
ROM
122
2
COMPUTER

FIG. 1

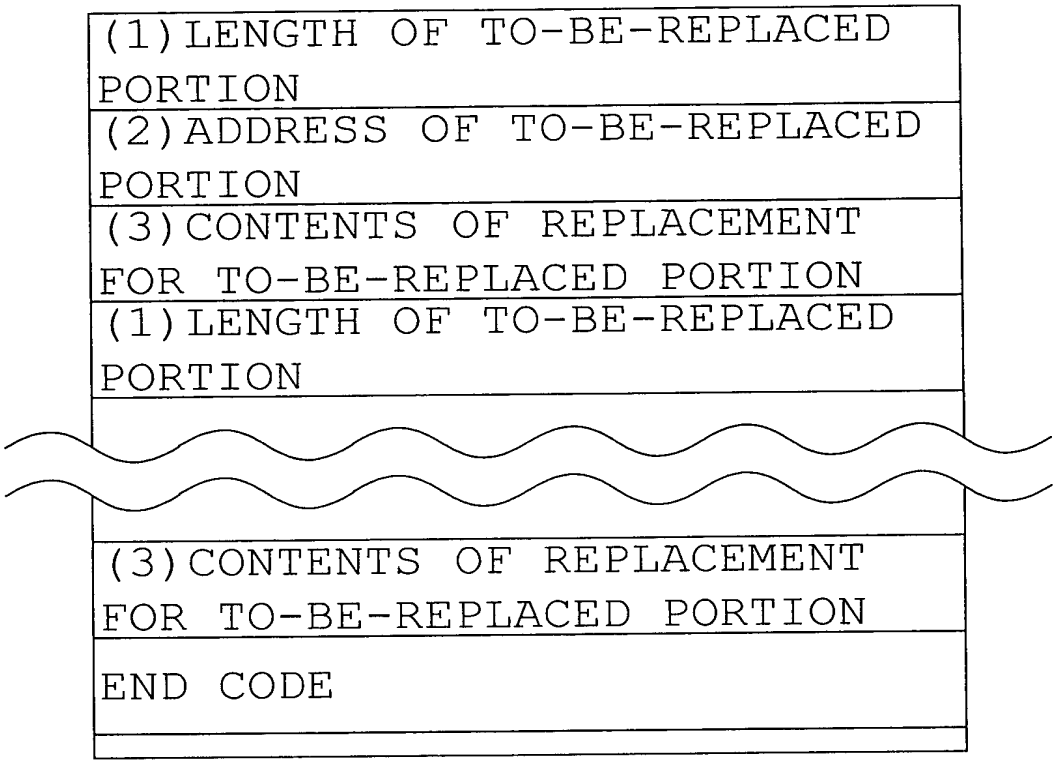

(1)LENGTH OF TO-BE-REPLACED PORTION
(2)ADDRESS OF TO-BE-REPLACED PORTION
(3)CONTENTS OF REPLACEMENT FOR TO-BE-REPLACED PORTION
(1)LENGTH OF TO-BE-REPLACED PORTION
(3)CONTENTS OF REPLACEMENT FOR TO-BE-REPLACED PORTION
END CODE

FIG. 2

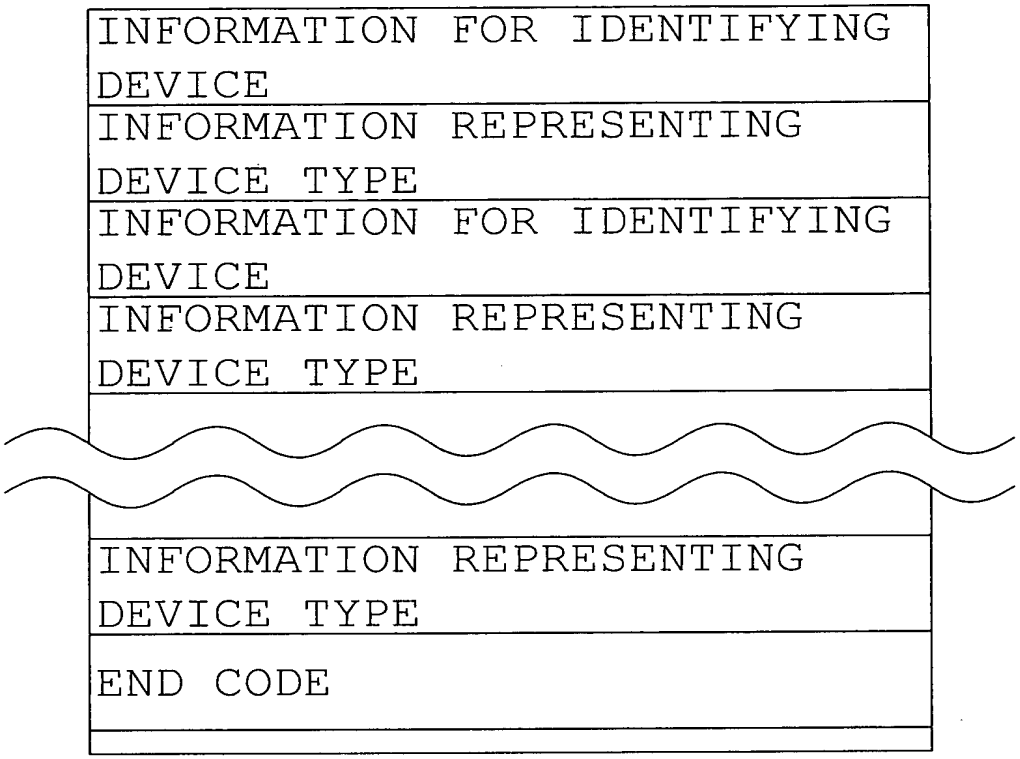

INFORMATION FOR IDENTIFYING DEVICE
INFORMATION REPRESENTING DEVICE TYPE
INFORMATION FOR IDENTIFYING DEVICE
INFORMATION REPRESENTING DEVICE TYPE
INFORMATION REPRESENTING DEVICE TYPE
END CODE

FIG. 3

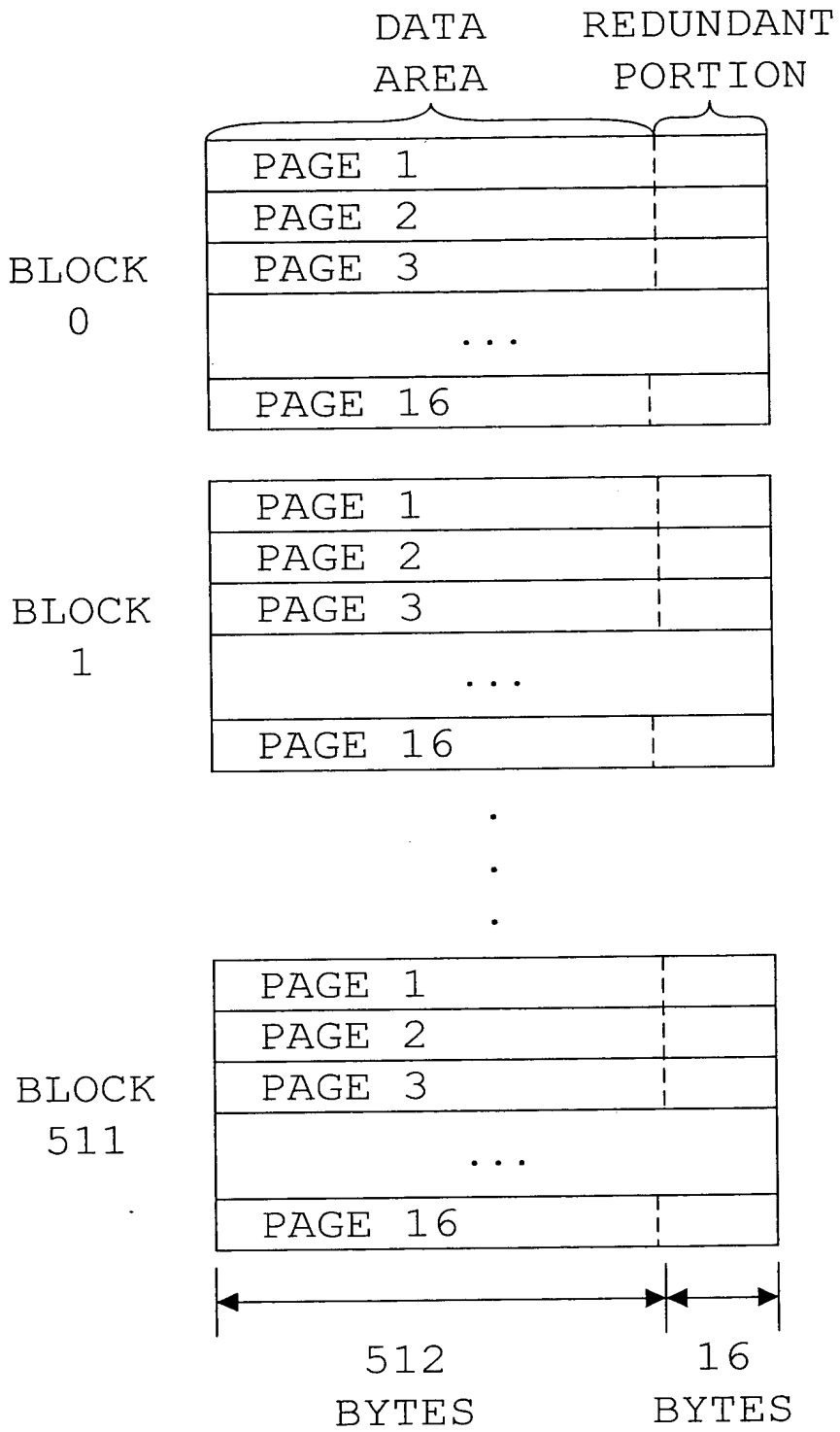

DATA AREA
REDUNDANT PORTION
BLOCK 0
PAGE 1
PAGE 2
PAGE 3
...
PAGE 16
BLOCK 1
PAGE 1
PAGE 2
PAGE 3
...
PAGE 16
BLOCK 511
PAGE 1
PAGE 2
PAGE 3
...
PAGE 16
512 BYTES
16 BYTES

FIG. 4

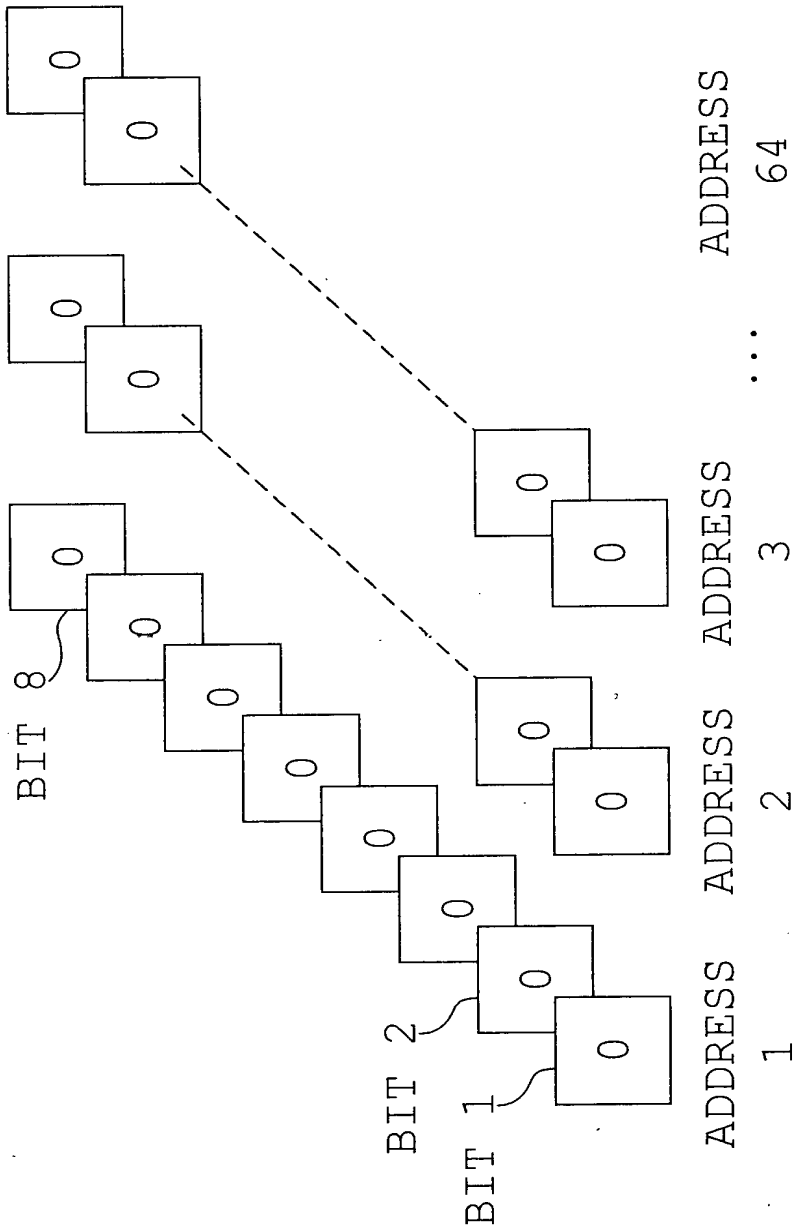
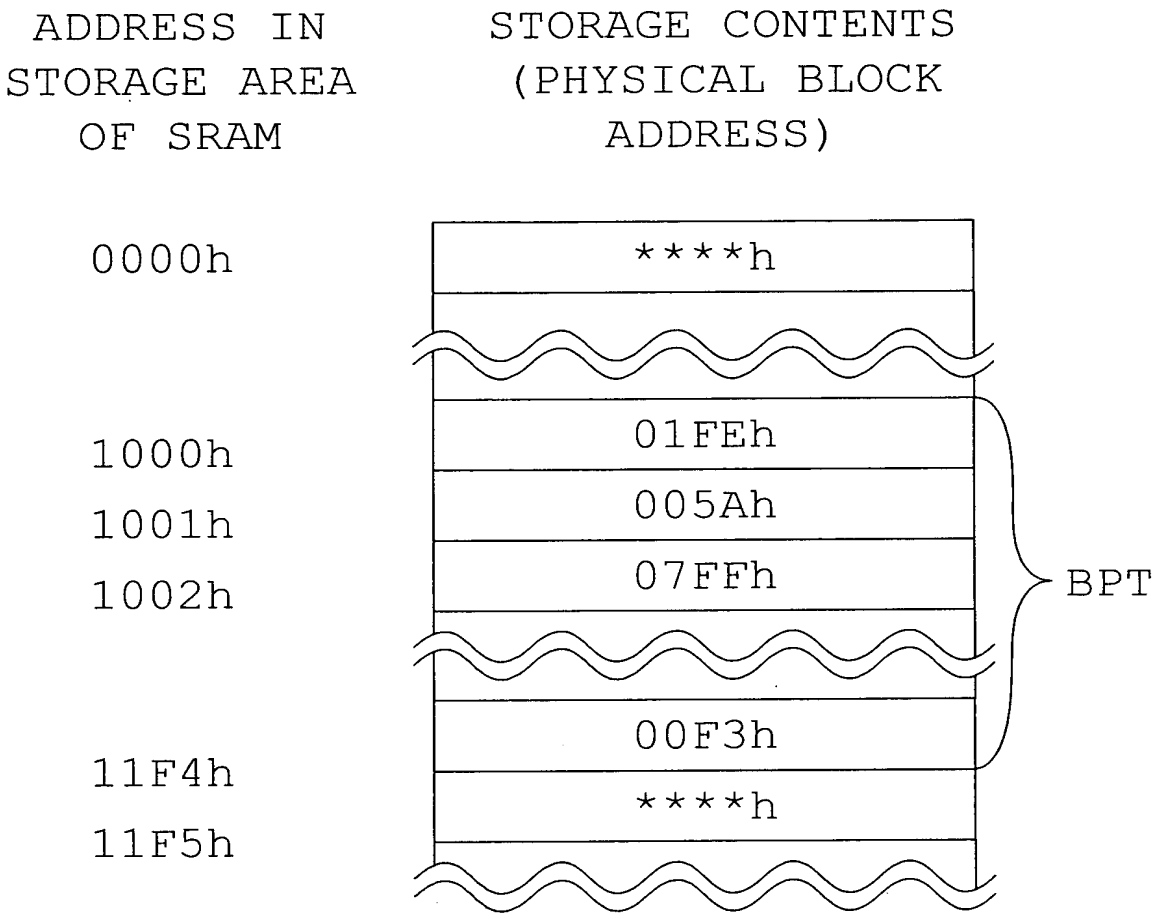


FIG. 5



(ADDRESS IN STORAGE AREA OF SRAM)
=(LOGICAL BLOCK ADDRESS)+1000h

FIG. 6

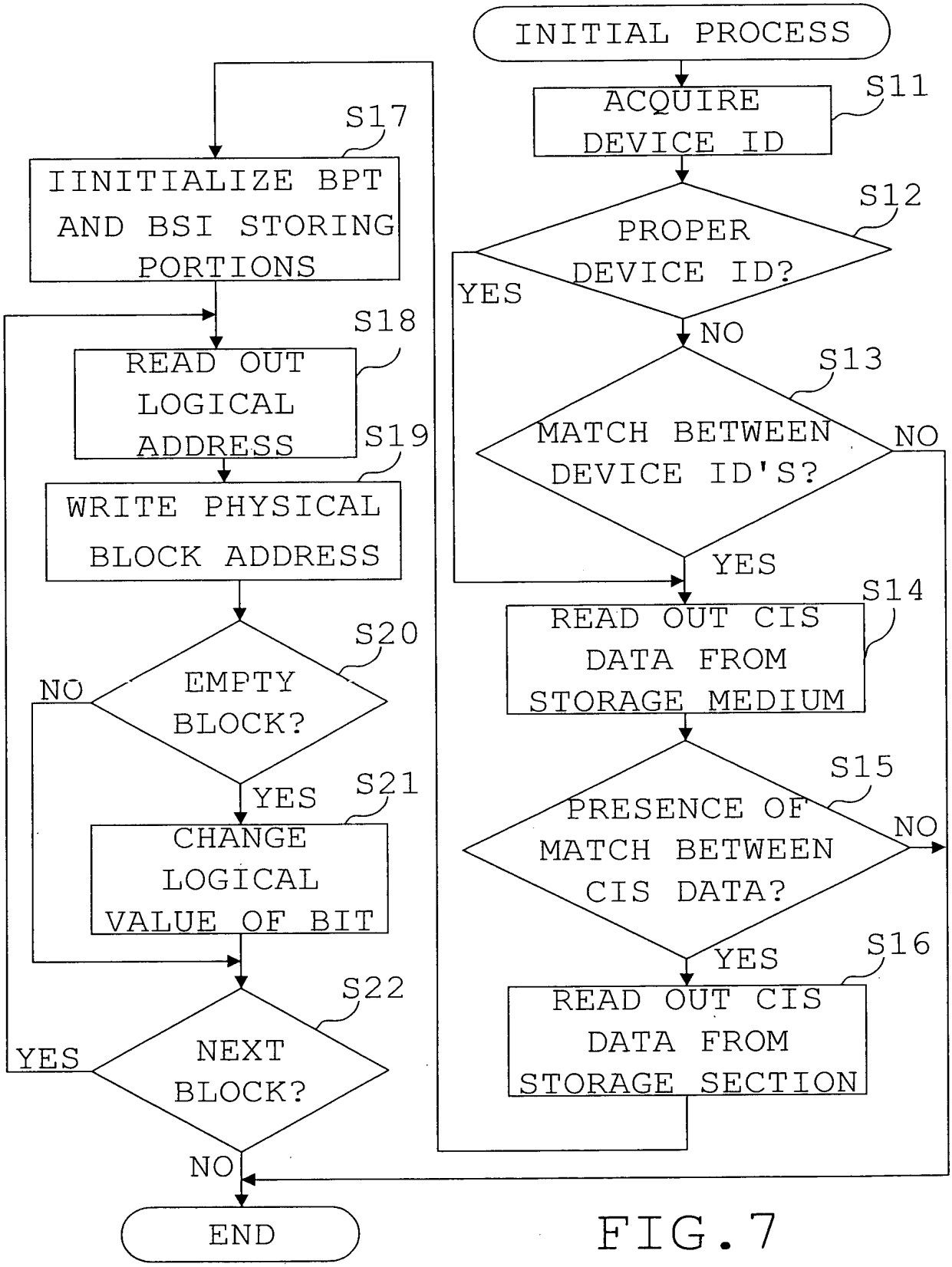
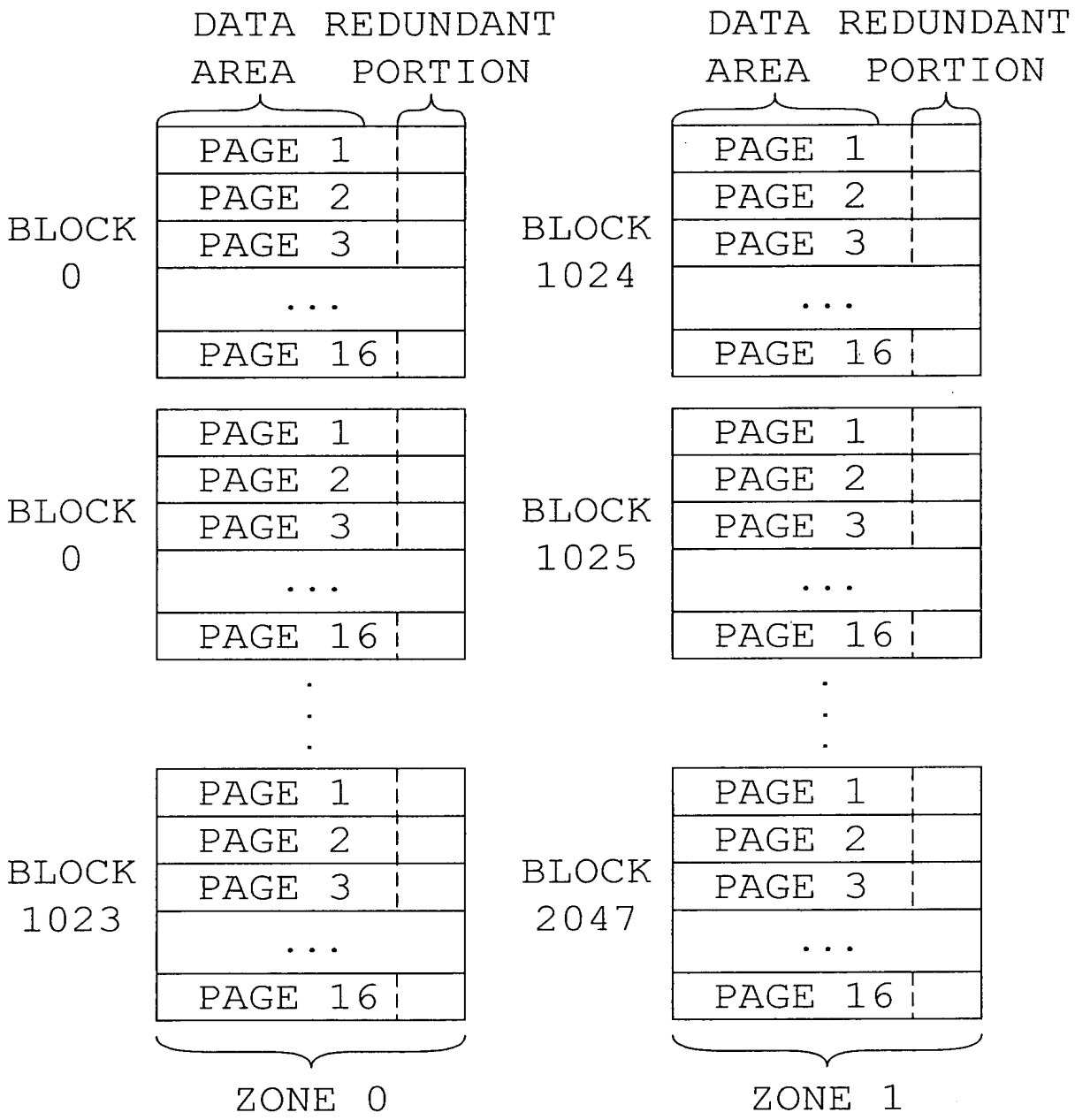

INITIAL PROCESS
ACQUIRE DEVICE ID
S11
PROPER DEVICE ID?
S12
YES
NO
MATCH BETWEEN DEVICE ID'S?
S13
NO
YES
READ OUT CIS DATA FROM STORAGE MEDIUM
S14
PRESENCE OF MATCH BETWEEN CIS DATA?
S15
NO
YES
READ OUT CIS DATA FROM STORAGE SECTION
S16
IINITIALIZE BPT AND BSI STORING PORTIONS
S17
READ OUT LOGICAL ADDRESS
S18
WRITE PHYSICAL BLOCK ADDRESS
S19
EMPTY BLOCK?
S20
NO
YES
CHANGE LOGICAL VALUE OF BIT
S21
NEXT BLOCK?
S22
YES
NO
END

FIG. 7



LOGICAL BLOCK ADDRESS ASSIGNED TO BLOCKS BELONGING TO ZONE 0: 0000h TO 03E7h
LOGICAL BLOCK ADDRESS ASSIGNED TO BLOCKS BELONGING TO ZONE 1: 03E8h TO 07CFh

FIG. 8

9/10

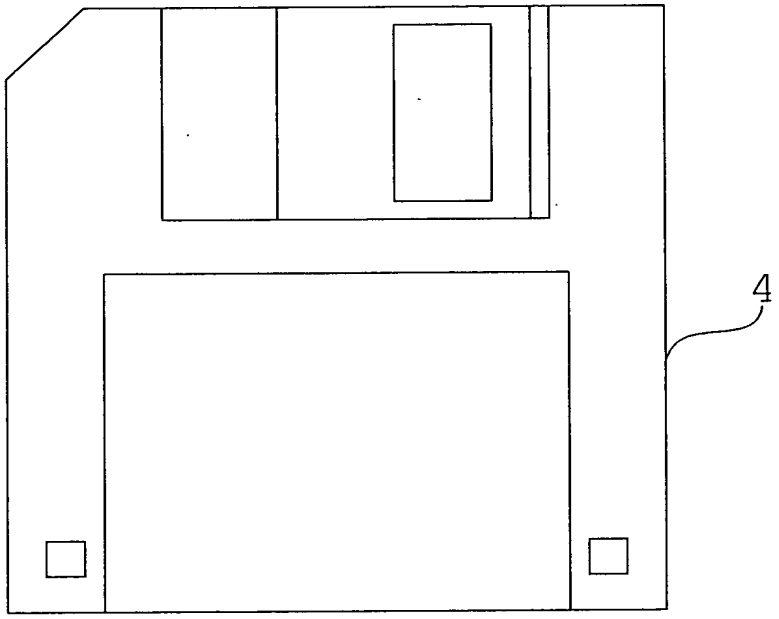


FIG. 9

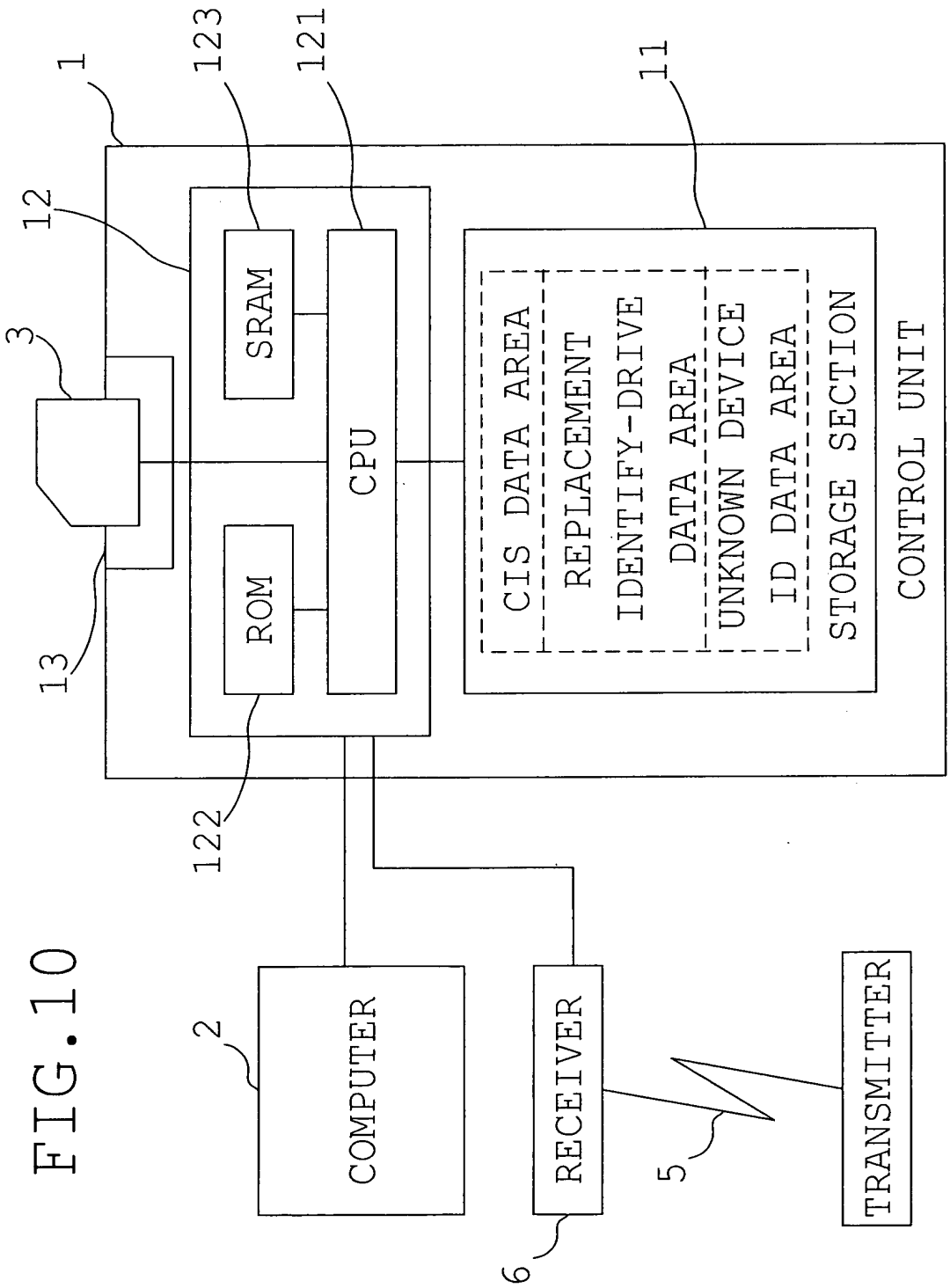

1
123
121
11
12
3
SRAM
CPU
CIS DATA AREA
REPLACEMENT
IDENTIFY-DRIVE
DATA AREA
UNKNOWN DEVICE
ID DATA AREA
STORAGE SECTION
CONTROL UNIT
13
ROM
122
2
COMPUTER
RECEIVER
5
TRANSMITTER
6

FIG. 10